

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1408	pipelin\$4 near2 full	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/25 15:04
L2	4	((pipelin\$4 near2 full) and ((cipher block chaining) or (digital encryption standard)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/25 15:05
L3	4	((pipelin\$4 near2 full) and ((cipher block chaining) or (digital encryption standard))) and feedback	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/25 15:08
L5	149	((pipelin\$4 near2 full) and ((cbc) or (\$3DES))) and feedback and (encryption or \$2cipher\$4 or scrambl\$4 or chryptography)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/25 15:11
L6	103	((pipelin\$4 near2 full) and ((cbc) or (DES))) and feedback and (encryption or \$2cipher\$4 or scrambl\$4 or chryptography)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/25 15:19
L7	368	380/29.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/04/25 15:19
S1	443	"cipher block chaining"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/02/24 06:55
S3	203	"cipher block chaining" and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 11:25
S9	29	"cipher block chaining" with feedback and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 10:41

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S12	1	"cipher block chaining" with (different adj2 (vector or variable)) and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 11:47
S13	0	"cipher block chaining" with (plurality adj2 (vector or variable)) and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 11:47
S14	0	"cipher block chaining" with (multiple adj2 (vector or variable)) and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 11:48
S15	4	"cipher block chaining" with ((multiple or plurality or more) with (vector or variable)) and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 12:18
S16	2	"cipher block chaining" with ((multiple or plurality or more) with (initial adj3 (vector or variable))) and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 12:19
S17	2	"cipher block chaining" same ((multiple or plurality or more) with (initial adj3 (vector or variable))) and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 12:19
S18	2	"cipher block chaining" and ((multiple or plurality) with (initial adj3 (vector or variable))) and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 12:20
S19	113	((multiple or plurality) with (initial adj3 (vector or variable))) and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 12:20
S20	2	index\$3 near8 ((multiple or plurality) with (initial adj3 (vector or variable))) and @ad <= "20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 15:50
S21	2	"4969190".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 15:51

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S22	2	"5835599".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 18:30
S23	624	"DES" with "extra round" or "extra stage"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 18:31
S24	0	"DES" with ("extra round" or "extra stage")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 18:32
S26	1	"DES" same ("extra round" or "extra stage")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 18:33
S27	702	("extra round" or "extra stage")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 18:33
S28	1	("extra round" or "extra stage") and "data encryption system" and @ad <="20001006"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/07 18:34
S29	2	"5432849".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/09 16:08
S30	2	"6055316".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/12/09 16:15
S31	496	"cipher block chaining"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/05/20 09:19
S32	540	pipeline full	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 06:55

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S33	41	(pipeline full) same (speed or fast)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 06:56
S34	19	pipeline full same (speed or fast) and ("DES")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 06:57
S35	56	("data encryption standard" or "DES" or "AES" or "Advanced Encryption Standard") same (initial vectors)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 14:28
S36	0	("data encryption standard" or "DES" or "AES" or "Advanced Encryption Standard") same (initial vectors) and (pipeline full)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 14:30
S37	0	("data encryption standard" or "DES" or "AES" or "Advanced Encryption Standard") and (initial vectors) and (pipelin\$4 near3 full)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 14:45
S39	561	(pipelin\$4 full)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 14:32
S40	96	(pipelin\$4 full) and des	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 14:32
S41	8	(pipelin\$4 full) and (data encryption standard)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 14:32
S42	0	("data encryption standard" or "DES" or "AES" or "Advanced Encryption Standard") and (initial vectors) and (pipelin\$4 near3 full) and (vectors)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 14:33

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S43	0	("data encryption standard" or "DES" or "AES" or "Advanced Encryption Standard") and (initial vectors) and (pipelin\$4 near3 full) and ((initial vectors) or (control vectors) or (initial variables))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 14:35
S44	21	(pipelin\$4 near3 full) and ((initial vectors) or (control vectors) or (initial variables))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 14:35
S45	28	("data encryption standard" or "DES" or "AES" or "Advanced Encryption Standard") and (initial vectors) and (pipelin\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 15:48
S46	0	predetermined number of (stage or round)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 15:49
S47	842	predetermined near number near (stage or round)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 15:49
S48	2	predetermined near number near (stage or round) same ("DES" or (data encryption standard))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 15:50
S49	2	pre\$1determined near number near (stage or round) same ("DES" or (data encryption standard))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 15:50
S50	202	\$4determined near4 (stage or round) same ("DES" or (data encryption standard))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 15:51
S51	0	(\$4determined near context\$1) same \$4determined near4 (stage or round) same ("DES" or (data encryption standard))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/02/24 15:53

## EAST Search History

S52	744	pipeline near full	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/08/11 10:02
S53	1	pipeline near full same feed\$1back\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2006/08/11 10:02



Search Result - Print Format

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**Key:** IEEE JNL = IEEE Journal or Magazine, IEEE JNL = IEEE Journal or Magazine, IEEE CNF = IEEE Conference, IEEE CNF = IEEE Conference, IEEE STD = IEEE Standard

1. **VLSI implementations of the triple-DES block cipher**  
Kitsos, P.; Goudevenos, S.; Koufopavlou, O.;  
Electronics, Circuits and Systems, 2003. ICECS 2003. Proceedings of the 2003 10th IEEE International Conference on  
Volume 1, 14-17 Dec. 2003 Page(s):76 - 79 Vol.1  
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2. **Implementation of an FPGA based accelerator for virtual private networks**  
Cheung, O.Y.H.; Leong, P.H.W.;  
Field-Programmable Technology, 2002. (FPT). Proceedings. 2002 IEEE International Conference on  
16-18 Dec. 2002 Page(s):34 - 41  
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3. **Chip-package Co-implementation of a triple DES Processor**  
Schaffer, T.; Glaser, A.; Franzon, P.D.;  
Advanced Packaging, IEEE Transactions on [see also Components, Packaging and Manufacturing Technology, Part B: Advanced Packaging, IEEE Transactions on]  
Volume 27, Issue 1, Feb. 2004 Page(s):194 - 202  
IEEE JNL
4. **Secure scalable streaming and secure transcoding with JPEG-2000**  
Wee, S.; Apostolopoulos, J.;  
Image Processing, 2003. ICIP 2003. Proceedings. 2003 International Conference on  
Volume 1, 14-17 Sept. 2003 Page(s):1 - 205-8 vol.1  
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5. **Set of symmetric block ciphering Soft-Cores**  
Melnik, V.;  
CAD Systems in Microelectronics, 2003. CADSM 2003. Proceedings of the 7th International Conference. The Experience of Designing and Application of  
18-22 Feb. 2003 Page(s):190 - 193  
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6. **FPGA Implementations of the DES and Triple-DES Masked Against Power Analysis Attacks**  
F.-X. Standaert; G. Rouvroy; J.-J. Quisquater;  
Field Programmable Logic and Applications, 2006. FPL '06. International Conference on  
Aug. 2006 Page(s):1 - 4  
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7. **5 GHz goes the distance for home networking**  
Fowler, P.;  
Microwave Magazine, IEEE  
Volume 3, Issue 3, Sept. 2002 Page(s):49 - 55  
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8. **Speech privacy technophobes need not apply**  
Caloyannides, M.A.;  
Security & Privacy Magazine, IEEE  
Volume 2, Issue 5, Sept.-Oct. 2004 Page(s):86 - 87

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**9. Triple-DES ASIC Module for a Power-Smart System-on-Chip Architecture**

Patel, D.; Muresan, R.;  
Electrical and Computer Engineering, Canadian Conference on  
May 2006 Page(s):1069 - 1072

IEEE CNF

**10. Auto-pipe and the X language: a pipeline design tool and description language**

Franklin, M.A.; Tyson, E.J.; Buckley, J.; Crowley, P.; Maschmeyer, J.;  
Parallel and Distributed Processing Symposium, 2006. IPDPS 2006. 20th International  
25-29 April 2006 Page(s):10 pp.

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**11. Clarifying obfuscation: improving the security of white-box DES**

Link, H.E.; Neumann, W.D.;  
Information Technology: Coding and Computing, 2005. ITCC 2005. International Conference on  
Volume 1, 4-6 April 2005 Page(s):679 - 684 Vol. 1

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**12. Implementation conditional-access system for pay TV based on Java card**

Prasertsatid, N.; Sookchareonphol, D.; Kosalwit, S.;  
TENCON 2004. 2004 IEEE Region 10 Conference  
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**13. Implementation conditional access system for pay TV based on Java card**

Prasertsatid, N.;  
Computational Electromagnetics and Its Applications, 2004. Proceedings. ICCEA 2004. 2004 3rd International  
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**14. Configurable hardware implementation of triple-DES encryption algorithm for wireless local area network**

Hamalainen, P.; Hannikainen, M.; Hamalainen, T.; Saarinen, J.;  
Acoustics, Speech, and Signal Processing, 2001. Proceedings. (ICASSP '01). 2001 IEEE International Conference  
Volume 2, 7-11 May 2001 Page(s):1221 - 1224 vol.2

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**15. Design and evaluation of high security contact/contactless smart IC cards**

Tsuru, K.; Murata, K.; Ohtani, Y.; Nagai, Y.;  
Industrial Electronics Society, 2001. IECON '01. The 27th Annual Conference of the IEEE  
Volume 1, 29 Nov.-2 Dec. 2001 Page(s):76 - 79 vol.1

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**16. Design and implementation of a private and public key crypto processor and its application to a security system**

Ho Won Kim; Sunggu Lee;  
Consumer Electronics, IEEE Transactions on  
Volume 50, Issue 1, Feb 2004 Page(s):214 - 224

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**17. Performance evaluation of three encryption/decryption algorithms**

Kofahi, N.A.; Turki Al-Somani; Khalid Al-Zamil;  
Circuits and Systems, 2003. MWSCAS '03. Proceedings of the 46th IEEE International Midwest Symposium on  
Volume 2, 27-30 Dec. 2003 Page(s):790 - 793 Vol. 2

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18. **A Novel VLSI Architecture for an Integrated Crypto Processor**  
Sathishkumar, G.A.; Prasanna, C.;  
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11-13 Dec. 2005 Page(s):272 - 275  
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19. **A stream cipher algorithm based on conventional encryption techniques**  
Ya-Ping Zhang; Jizhou Sun; Xu Zhang;  
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20. **Developing a cryptology algorithm by using wavelet transform**  
Kucuk, F.; Kucuk, M.; Seker, S.;  
Signal Processing and Communications Applications Conference, 2004. Proceedings of the IEEE 12th  
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21. **Compact and efficient encryption/decryption module for FPGA implementation of the AES Rijndael very well suited for small embedded applications**  
Rouvroy, G.; Standaert, F.-X.; Quisquater, J.-J.; Legat, J.-D.;  
Information Technology: Coding and Computing, 2004. Proceedings. ITCC 2004. International Conference on  
Volume 2, 2004 Page(s):583 - 587 Vol.2  
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22. **Why triple des with 128-bit key and not rijndael should be aes**  
Rehman, H.; Jamshed, S.; Absar ul Haq;  
Students Conference, 2002. ISCON '02. Proceedings. IEEE  
Volume 2, Aug. 16-17, 2002 Page(s):12 - 13  
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23. **Performance study of software-based iSCSI security**  
Shuang-Yi Tang; Ying-Ping Lu; Du, D.H.C.;  
Security in Storage Workshop, 2002. Proceedings. First International IEEE  
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24. **CryptoManiac: a fast flexible architecture for secure communication**  
Wu, L.; Weaver, C.; Austin, T.;  
Computer Architecture, 2001. Proceedings. 28th Annual International Symposium on  
30 June-4 July 2001 Page(s):110 - 119  
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25. **A 6805-based security system for broadcasting stock information**  
Chung-Huang Yang;  
Security Technology, 2000. Proceedings. IEEE 34th Annual 2000 International Carnahan Conference on  
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